IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re	U.S. Patent Application)	•
ODA et al.)	
)	
Appıı	cation Number: To be Assigned)	Art Unit 2811
Filed:	Concurrently herewith	j	0
For:	SEMICONDUCTOR DEVICE AND)	
	MANUFACTURING METHOD)	
Attor	ney Docket No. HITA.0474)	
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	rable Assistant Commissioner Patents		
Wash	ington D.C. 20231		

INFORMATION DISCLOSURE STATEMENT

The above-referenced application is a Divisional of U.S. Serial No. 09/824,225 filed April 3, 2001. It includes the same disclosure as U.S. patent application Serial No. 09/824,225.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

This Information Disclosure Statement is submitted with the above-captioned U.S. Divisional application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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December 18, 2003

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Information Disclosure Statement by Applicant			FILING DATE	GROUP	GROUP					
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		U	J.S. Patent Docu	iments						
Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS		FILING DATE		
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DATE CONSIDERED

Examiner: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant

* Cited by Applicant in Parent
** Cited by Examiner in Parent

EXAMINER